

# Formal methods: Lecture 2

11.02.2015

Model Checking I:  
TRANSITION SYSTEMS

# Model Checking (MC) problem: intuition

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- ▶ Correct design means that certain correctness properties must be satisfied by the system to be developed
- ▶ Correctness properties state what behaviours/features are correct and what are not in the system.
- ▶ To apply rigorous verification methods both
  - ▶ system description and
  - ▶ correctness properties descriptionmust be formalised
- ▶ System is described formally with its model
- ▶ Properties are specified formally as logic expressions.

# Model Checking (formally)

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- ▶ Satisfaction relation symbolically:

$$M \models \varphi ?$$

“Does model  $M$  satisfy logic expression  $\varphi$ ?”

- ▶ Property  $\varphi$  is stated often in temporal logic
- ▶  $M$  is a state-transition system that models the behavior of the implementation to be verified

## Procedural view:

- ▶ Model checking is an enumeration method of the state space of  $M$  to determine if it satisfies the property  $\varphi$ .

# Advantage of MC

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- ▶ Fully automatic
- ▶ Diagnostic trace (counter example) generated by checker helps to analyze the source of the problem
- ▶ Good for bug-hunting, i.e a “debugger” that does not require full execution of your program.

# Modelling

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## How to get $M$ ?

1. By the process of abstraction:
  - ▶ Makes verification possible by retaining the part of the system that is relevant to modeling;
  - ▶ Should not discard too much so that the result lacks certainty, or too little so that the verification is not feasible;
  - ▶ Usually done by human (novel automatic model extraction techniques are gaining popularity).
2. By observation and learning (model learning)

# Choice of models

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- ▶ We focus on state-transition systems. They are
  - ▶ acceptable by model checkers;
  - ▶ mostly finite set of states and transitions;
  - ▶ also push-down automata/systems are possible;
  - ▶ source programs can also be used as models, e.g., Pathfinder for Java code;
  - ▶ in symbolic encoding the logic formula specify abstract properties instead of explicit state behavior modelling.

# Modeling notions

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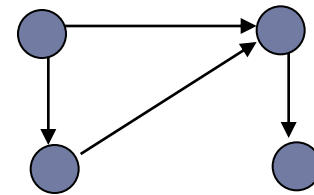
- ▶ **State**

- ▶ We want to express what is true in a particular state
- ▶ A *state* is a “snapshot” of the system variables’ valuation(s).

- ▶ **Transition** represents relation between states.

Can be an abstraction of

- ▶ **C program** statement, e.g. `x++`;
- ▶ an electronic circuit
- ▶ or just an arrow, the source and destination states of which matter.



# Atomicity

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- ▶ Atomic transition – uninterruptable when started
- ▶ Atomicity determines the abstraction level of the model
  - ▶ too big step may miss intermediate states that are relevant;
  - ▶ too small step may blow up the model unnecessarily.
- ▶ Atomicity of transitions must consider concurrency
  - ▶ possible interleavings of transitions and interactions must be explicit.



# Kripke Structure ( $KS$ )

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One of the classical STSs

4-tuple  $(S, S_0, L, R)$  over a set of atomic propositions  $AP$   
where

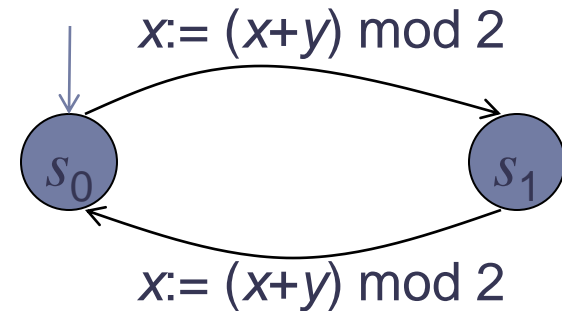
- ▶  $S$  is a set of (control) states
- ▶  $S_0$  is initial state
- ▶  $L$  is a labeling function:  $S \rightarrow 2^{AP}$
- ▶  $R$  is the transition relation:  $S \times S$

# Example of $KS$

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Assume in  $s_0$   $x=1$  and  $y=1$

- ▶  $S = \{s_0, s_1\}$
- ▶  $S_0 = \{s_0\}$
- ▶  $R = \{(s_0, s_1), (s_1, s_0)\}$
- ▶  $L(s_0) = \{x=1, y=1\}$
- ▶  $L(s_1) = \{x=0, y=1\}$



# Modeling Reactive System

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- ▶ Reactive systems:
  - ▶ do not terminate;
  - ▶ interact with their environment constantly;
  - ▶ *KS* is just one way of modeling them.
- ▶ Consider *KS* as a simple modeling language for RS-s.

# Properties of reactive systems to verify

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- ▶ *race condition* - the output depends on the sequence of uncontrollable events. It becomes a *bug* when events do not happen in the order the programmer intended, e.g.
  - ▶ in file systems, programs may "collide" in their attempts to modify or access a file, which could result in data corruption;
  - ▶ in networking, two users of different servers on different ends of the network try to start the same-named channel at the same time.
- ▶ *deadlock* – all processes are infinitely waiting after each other for releasing the resources. Generally undecidable, practical only for finite state processes.
- ▶ *starvation* - blocking resources for only some processes.
- ▶ etc.

# Modeling Concurrent Programs with *KS*

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- ▶ Steps of constructing KS from a program (by Manna, Pnueli):
  1. Abstract (sequential) component programs as logic relations.
  2. Compose the logic relations for the *concurrent program*.
  3. Compute a Kripke structure from the logic relations.

How it works in practice?

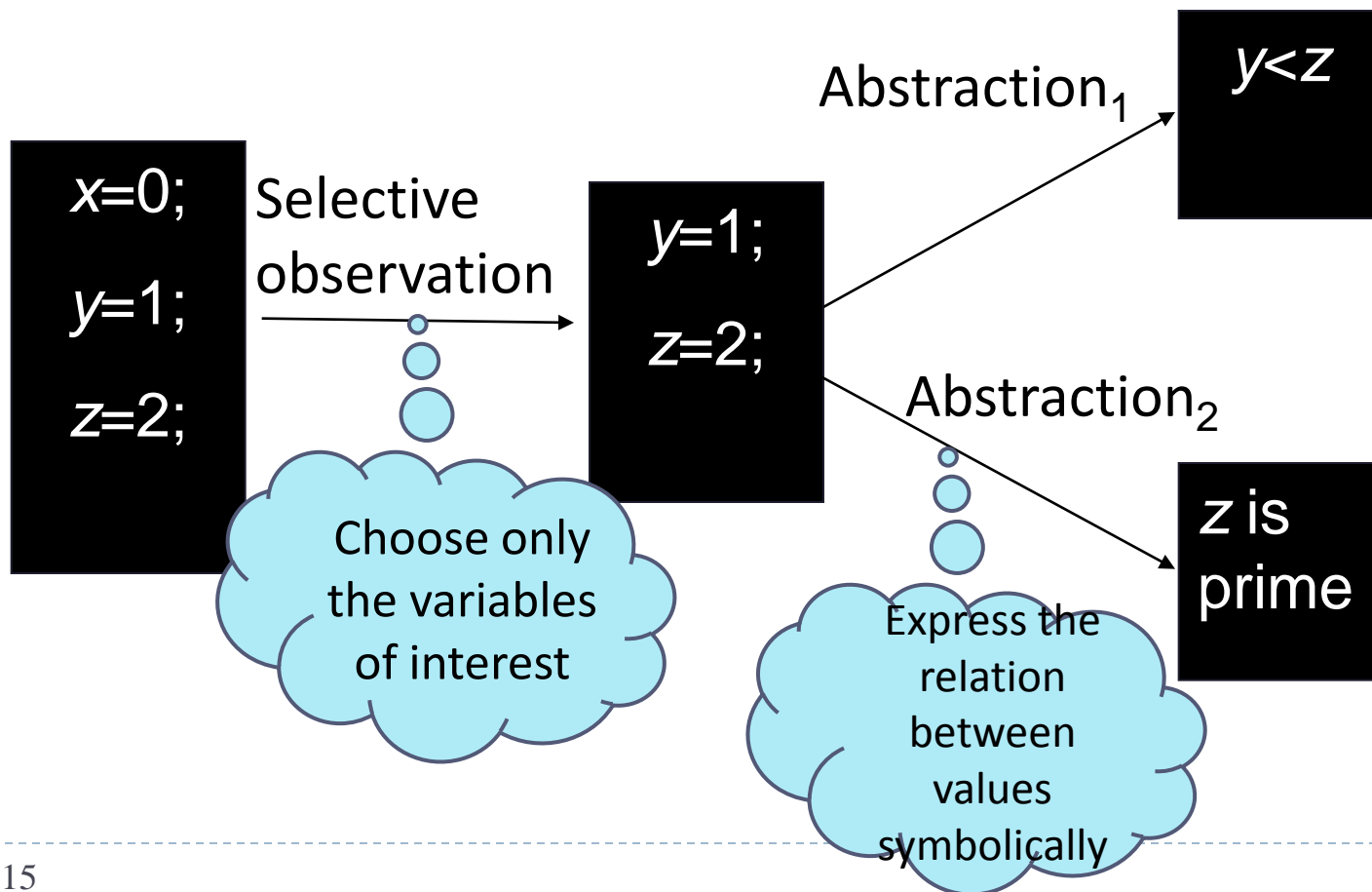
# Describing States

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For abstracting states we use program variables and 1st order predicate logic...

- ▶ true, false,  $\neg$ ,  $\wedge$ ,  $\vee$ ,  $\forall$ ,  $\exists$ ,  $\rightarrow$
- ▶ extended with equality “=” and interpreted predicate symbols and function symbols:
  - ▶  $x = y$
  - ▶ even ( $x$ )
  - ▶ odd ( $x$ )
  - ▶ prime ( $x$ )
  - etc

# Example of state abstraction steps



# Representing States

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- ▶ *Valuation* of a state
  - ▶ A mapping:  $V \rightarrow \mathbf{V}$  from observable state variables  $V$  to their value domain  $\mathbf{V}$ ,
- ▶ *Symbolic state* = set of explicit states
  - ▶ the set of states is described by a 1st order logic formula
  - ▶ Instead of enumerating explicit states we use a logic formula describing the set  $S_0$
  - ▶ Example:  $S_0 \equiv (x = 1) \wedge (y > 2)$



# Representing a transition

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- ▶ Transition abstracts a program command (or circuit)
  - ▶ Distinguish two sets of variables' values:  
 $V$  and  $V'$  for variable valuation in pre- and post-state of the transition, respectively
- ▶ Transition relation is a relation between  $V$  and  $V'$ 
  - ▶ relation is expressible as a set of pairs of states
  - ▶ represented as a logic formula on  $V, V'$  with "=",
- ▶ Example:
  - ▶ Relation  $x' = x+1$  describes the effect of program statement  $x:=x+1$

# From Logic Relation to Kripke Structure

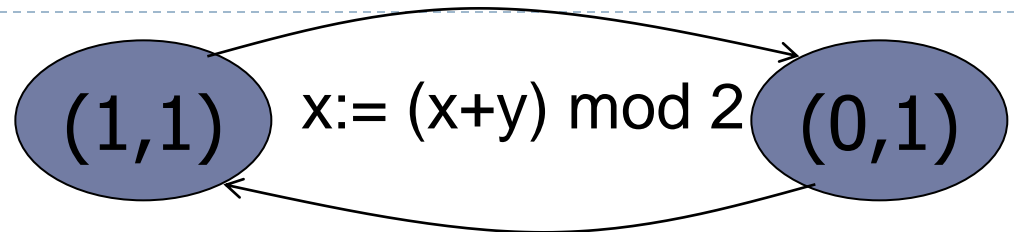
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## Rules

- ▶  $S$  (statespace) is the set of all valuations for  $V$ ;
- ▶  $S_0$  is the set of all valuations that satisfy  $\mathcal{S}_0$  (a logic formula)
- ▶ If  $s$  and  $s'$  are two states, s.t.  $(s, s') \in R(s, s')$  then the pair  $(s, s')$  is a transition in KS;
- ▶  $L$  is defined so that  $L(s)$  is the subset of all atomic propositions true in  $s$ .

# Example

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- ▶  $S_0 \equiv x = 1 \wedge y = 1$
  - ▶  $R \equiv x' = (x+y) \bmod 2$
  - ▶  $S = B \times B$ , where  $B = \{0,1\}$
- 
- ▶  $S_0 = \{(1,1)\}$
  - ▶  $R = \{((1,1), (0,1)), ((0,1), (1,1))\}$
  - ▶  $L(1,1) = \{x=1, y=1\}$
  - ▶  $L(0,1) = \{x=0, y=1\}$

# Abstracting parallel programs to KS

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- ▶ A parallel program contains sequential processes
  - ▶ with synchronization primitives: wait, lock and unlock
  - ▶ processes may share variables
  - ▶ no assumption about the speed and execution order of these processes
- ▶ Program commands are labeled by  $l_1 \dots l_n$
- ▶ We use  $C(l_1, P, l_2)$  to denote the logic relation of the transition that represents program  $P$ .

# How to compute transition relation for sequential program fragments?

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- ▶ Base case: atomic statements:

- ▶ skip      % has no effect on data variables
- ▶ assignment:  $x := e$

Let  $C$  describe valuations before and after executing  $P$ :  $x := e$

$$C(l_1, x := e, l_2) \equiv$$

$$pc = l_1 \wedge pc' = l_2 \wedge x' = e \wedge \text{same}(V \setminus \{x\})$$

- ▶  $\text{same}(Y)$  means  $y' = y$ , for all  $y \in Y$ .

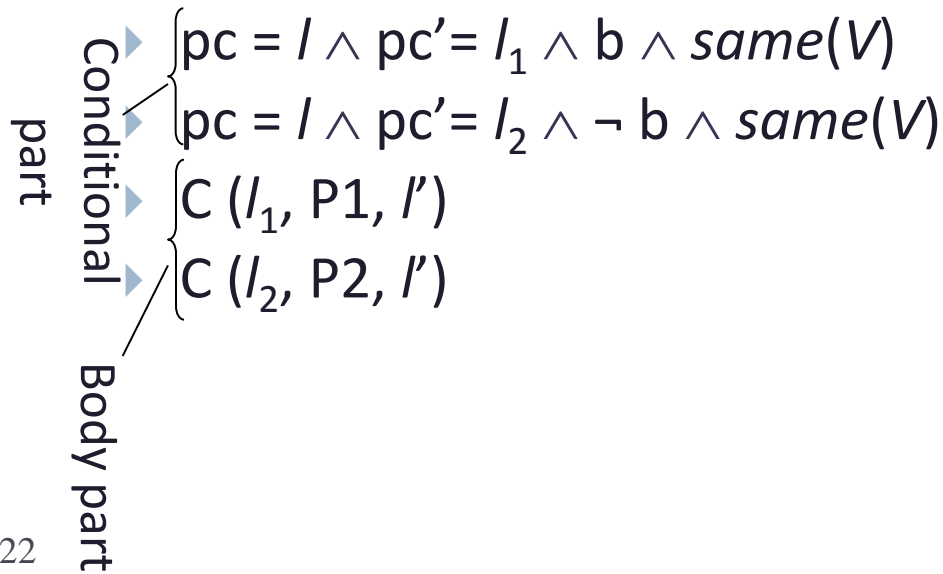
# How to compute transition relation for sequential program fragments? (2)

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- ▶ Sequential composition

$$C(l_0, P1 ; l : P2, l_1) = C(l_0, P1, l) \vee C(l, P2, l_1)$$

- ▶  $C(l, \text{if } b \text{ then } l_1 : P1 \text{ else } l_2 : P2 \text{ end if}, l')$  is the disjunction of:



# Example: concurrent while-loops sharing a variable "turn"

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```
L0: while (true) do
  NC0: wait (turn =0);
  CR0: turn := 1;
end while
```

L0'

```
L1: while (true) do
  NC1: wait (turn =1);
  CR1: turn := 0;
end while
```

L1'

- identify variables, including program counters
- compute set of states and set of initial states
- compute transitions

# Example (continued I)

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```
L0: while (true) do
  NC0: wait (turn =0);
  CR0: turn := 1;
end while
```

L0'

```
L1: while (true) do
  NC1: wait (turn =1);
  CR1: turn := 0;
end while
```

L1'

Identify variables, including program counters:

- $V = \{ pc\_0, pc\_1, turn \}$
- domain of **pc\_0** is L0, NC0, CR0, L0'
- domain of **turn** is {0,1}



# Example (continued II)

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```
L0: while (true) do
  NC0: wait (turn =0);
  CR0: turn := 1;
end while
```

L0'

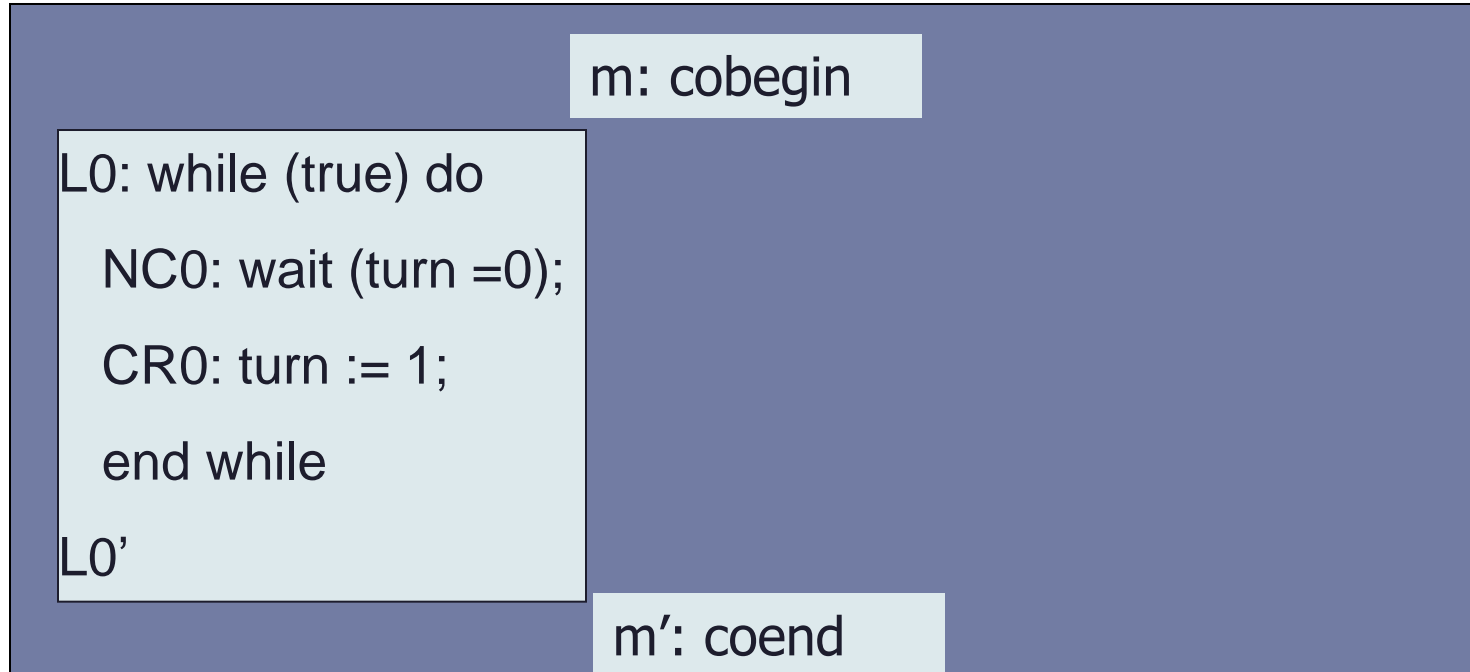
```
L1: while (true) do
  NC1: wait (turn =1);
  CR1: turn := 0;
end while
```

L1'

- ▶ Compute set of states and set of initial states
  - ▶  $S = \{(L0, L1, 1), (L0, L1, 0), (L0, NC1, 0), (L0, NC1, 1) \dots\}$
  - ▶  $S_0 = \{(L0, L1, 0), (L0, L1, 1)\}$

# Example (continued III)

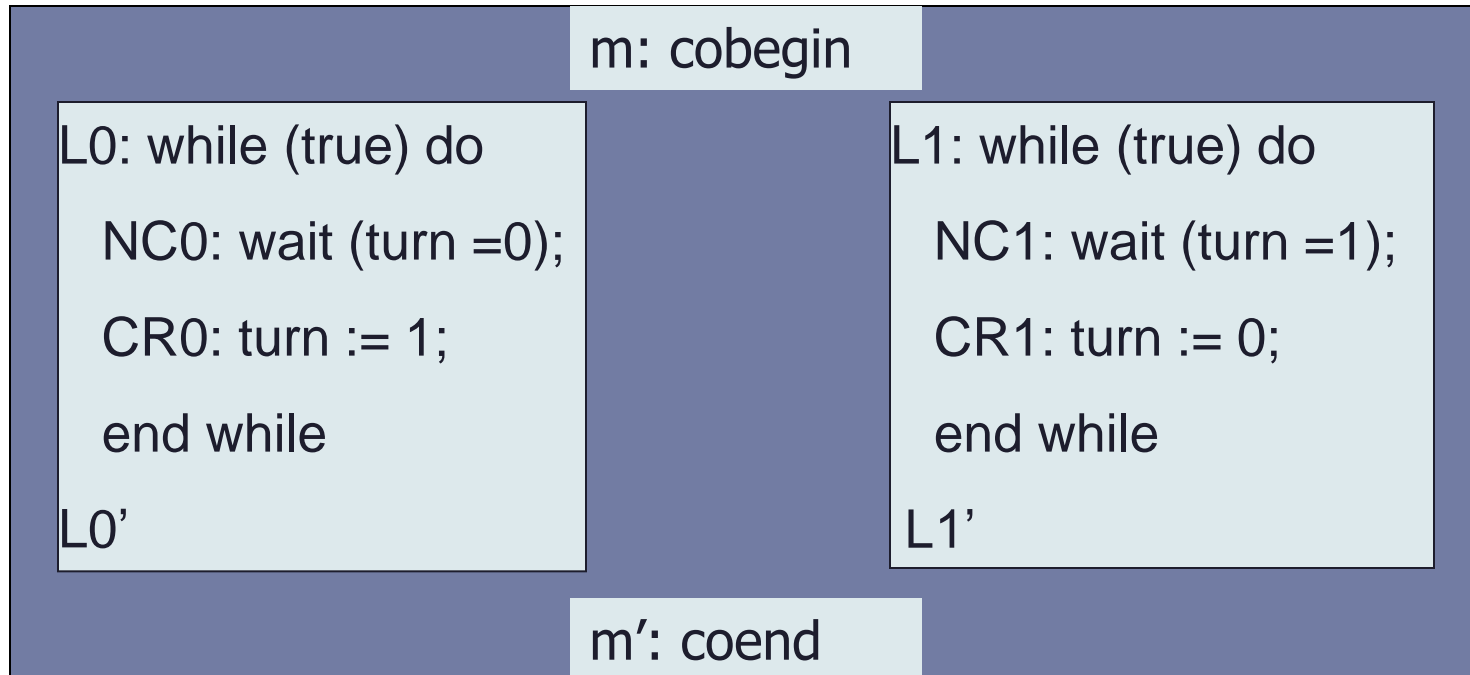
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- ▶ Compute transition relation separately & then compose them together:
  - ▶ For global program counter  $dom(pc) = \{m, m', \perp\}$
  - ▶  $\perp$  represents that one of local pc is taking effect.

# Example (continued IV)

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- ▶ Transition relations of the composition:

$$C(L0, P0, L0') \equiv \text{turn}' = \text{turn} + 1 \wedge \text{same}(V \setminus V0) \wedge \text{same}(PC \setminus PC0)$$

# Summary

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- ▶ Concept of MC (at very high level):
  - ▶ An automatic procedure that verifies temporal and state properties
  - ▶ Requires input:
    - ▶ a state transition system
    - ▶ a temporal property
- ▶ State transition system – Kripke structure (KS):
  - ▶ KS structure is our (teaching) language
  - ▶ *KS* models reactive systems
- ▶ An example demonstrated how a concurrent program is translated to *KS*:
  - ▶ Concurrent program to logic relations
  - ▶ Logic relations to *KS*.

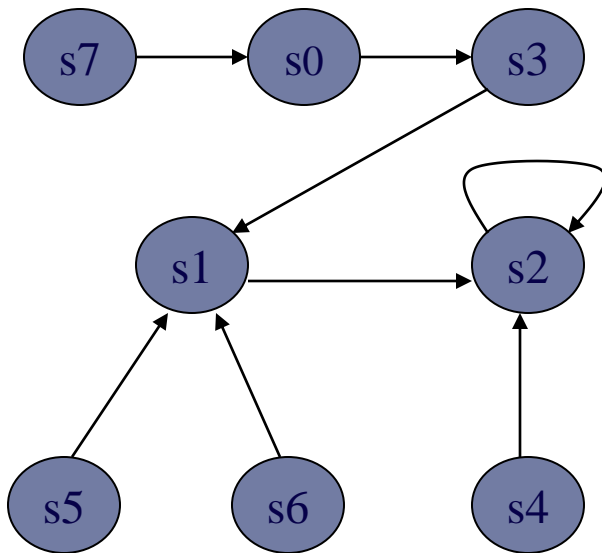
# Next lecture

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- ▶ Temporal properties
  - ▶ CTL\*, CTL and LTL
  - ▶ Their semantics
- ▶ CTL model checking on a Kripke structure

# Exercise

- ▶ Give your definition of APs  $p$ ,  $q$ ,  $r$ .



$$L(s_0) = \{\neg p, \neg q, \neg r\}$$

$$L(s_1) = \{\neg p, \neg q, r\}$$

$$L(s_2) = \{\neg p, q, \neg r\}$$

$$L(s_3) = \{\neg p, q, r\}$$

$$L(s_4) = \{p, \neg q, \neg r\}$$

$$L(s_5) = \{p, \neg q, r\}$$

$$L(s_6) = \{p, q, \neg r\}$$

$$L(s_7) = \{p, q, r\}$$